

<b>Education</b>	<b>B.S. Computer Engineering</b> <b>Missouri University of Science and Technology, Rolla, MO</b> Emphasis Area: Integrated Circuits and Logic Design Minor: Minor in Computer Science, Minor in Mathematics	<b>Graduated: July 2023</b> <b>GPA: 3.12/4.00</b>
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<b>Experience</b>	<b>Software Engineer I</b> <b>J.B. Hunt, Lowell, AR</b> <ul style="list-style-type: none"><li>- Utilized Java frameworks (Springboot, Lombok, and Kubernetes) to develop a functional web application</li><li>- Worked with industry standard DevOps practices such as CI/CD pipelines, GIT, and release coordination</li><li>- Resolved critical vulnerabilities in production code</li></ul>	<b>June 2023-Present</b>
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	<b>App Development Intern/Team Lead</b> <b>J.B. Hunt, Fayetteville, AR</b> <ul style="list-style-type: none"><li>- Managed a software team of four people in an Agile SCRUM framework to build ConfigMaps for Kubernetes clusters</li><li>- Worked with Docker containers and Kubernetes clusters to deploy web applications</li><li>- Built and designed a SQL database from schemas and Entity Relationship Diagrams</li></ul>	<b>June 2022-May 2023</b>
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	<b>President/Electrical Team Lead</b> <b>Solar Car Design Team, Missouri S&amp;T, Rolla, MO</b> <ul style="list-style-type: none"><li>- Managed a design team and coordinated events with university and community</li><li>- Handled and approved budgetary requirements</li><li>- Contributed to battery system management through testing and design</li></ul>	<b>September 2021-May 2023</b>
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<b>Skills &amp; Abilities</b>	<ul style="list-style-type: none"><li>- <b>Electronics Manufacturing:</b><ul style="list-style-type: none"><li>- Utilized soldering and digital multimeters to create an AVR Microcontroller development board</li><li>- Utilized a microcontroller and ultrasonic sensors to outfit an autonomous vehicle</li><li>- Designed an autonomous robot using a USB camera and single board computer (Raspberry Pi)</li></ul></li><li>- <b>Hardware:</b><ul style="list-style-type: none"><li>- Created a physical clock timer using Atmel AVR microcontrollers</li><li>- Designed combinational logic circuits using field programmable gate arrays (FPGAs)</li><li>- Designed digital circuit elements using programmable logic devices (PLDs)</li></ul></li><li>- <b>Software:</b><ul style="list-style-type: none"><li>- Worked with Siemens NX, Altera Quartus II, Altera ModelSim, Docker, Kubernetes Linux, Autodesk EAGLE, Microchip Studio 7, TI Energia, and GIT</li><li>- Modeled discrete time Fourier transforms in MATLAB</li><li>- Used WinCUPL to design combinational logic circuits to be programmed on a PLD</li></ul></li><li>- <b>Programming Languages and Scripting Languages:</b><ul style="list-style-type: none"><li>- Developed various machine learning and AI algorithms in Python to detect QR codes</li><li>- Utilized SQL and Python to create an automated chat room bot for scoring games</li><li>- Used R to analyze linear models</li></ul></li></ul>
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<b>Awards &amp; Honors</b>	<ul style="list-style-type: none"><li>- Missouri S&amp;T Miner and University Scholarships, 2019-2023</li><li>- National Society of Leadership and Success nomination, May 2021</li><li>- Missouri S&amp;T Honor Roll, 2019-2023</li></ul>
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